

**BIRCH, STEWART, KOLASCH & BIRCH, LLP**

**INTELLECTUAL PROPERTY LAW**  
**8110 GATEHOUSE ROAD**  
**SUITE 500 EAST**  
**FALLS CHURCH, VA 22042-1210**  
**USA**

(703) 205-8000

FAX (703) 205-8050  
(703) 698-8590 (G IV)

e-mail: [mailroom@bskb.com](mailto:mailroom@bskb.com)  
web: <http://www.bskb.com>

**CALIFORNIA OFFICE**  
**COSTA MESA, CALIFORNIA**

JAMES T. ELLER, JR.  
SCOTT L. LOWE  
D. RICHARD ANDERSON  
PAUL C. LEWIS  
MARK W. MILSTEAD\*  
RICHARD J. GALLAGHER  
JAYNE M. SAYDAH\*  
MARYANNE ARMSTRONG, PH D  
KECIA J. REYNOLDS  
ROBERT L. SCOTT, II  
KALPANA REDDY  
LARRY J. HUME

REG. PATENT AGENTS  
FREDERICK R. HANDREN  
MAKI HATSUMI  
CRAIG A. McROBBIE  
GARTH M. DAHLEN, PH D  
ROBERT E. GOOZNER, PH D  
MATTHEW J. LATTIG  
TIMOTHY R. WYCKOFF  
KRISTI L. RUPERT, PH D  
HARAY A. SAYADIAN, PH D  
MATTHEW T. SHANLEY  
JASON W. RHODES  
CHRISTINA F. ANNICK  
SUSAN W. GORMAN, PH D  
DAVID J. CHO

TERRELL C. BIRCH  
RAYMOND C. STEWART  
JOSEPH A. KOLASCH  
JAMES M. SLATTERY  
BERNARD L. SWEENEY\*  
MICHAEL K. MUTTER  
CHARLES GORENSTEIN  
GERALD M. MURPHY, JR.  
LEONARD R. SVENSSON  
BRYAN L. CLARK  
ANDREW D. MEIKLE  
JARC S. WEINER  
JIM KINNEY MUNCY  
ROBERT J. KENNEY  
RONALD J. DALEY  
JOHN W. BAILEY  
JOHN A. CASTELLANO  
GARY D. YACURA  
THOMAS S. AUCHTERLONIE  
MARK J. NUELLE, PH D

OF COUNSEL  
HERBERT M. BIRCH (1905-1996)  
ELLIOT A. GOLDBERG\*  
WILLIAM L. GATES\*  
EDWARD H. VALANCE  
RUPERT J. BRADY (RET.)\*  
F. PRINCE BUTLER  
FRED S. WHISENHUNT  
JAMES W. HELLWEGE

\*ADMITTED TO A BAR OTHER THAN VA

Date: August 30, 2001  
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Assistant Commissioner for Patents  
Box PATENT APPLICATION  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): UCHIDA, Ren

For: TESTING METHOD AND TESTING DEVICE FOR SEMICONDUCTOR  
INTEGRATED CIRCUITS

Enclosed are:

- X A specification consisting of 44 pages
- X 9 sheet(s) of formal drawings
- X An assignment of the invention - \$40.00 Recording Fee
- X Certified copy of Priority Document(s)
- X Executed Declaration X Original      Photocopy
- Applicant claims small entity status in accordance with 37 CFR 1.27
- Application Data Sheet in accordance with 37 C.F.R. 1.76



If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

BY Terrell C. Birch #2271  
TERRELL C. BIRCH  
Reg. No. 19,382

TERRELL C. BIRCH

Reg. No. 19,382

P. O. Box 747

Falls Church, Virginia 22040-0747

(703) 205-8000  
TCB/rem

**Figure 1** Schematic representation of the experimental design.